



10-02-02
RECEIVED
OCT -7 2002
TECHNOLOGY CENTER 2800.

Filed Via Express Mail pursuant to 37 C.F.R. 1.10

Record No.: EV 096 421 254 US

On: OCTOBER 1, 2002

By: *Linda E. Hastings*

LINDA E. HASTINGS

Any fee due as a result of this paper, not covered by an enclosed check, may be charged on Deposit Acct. No. 50-1290.

#10/B (NE)

10/10/02

Surles

Payton
11-21-02

Attorney Docket No.:NECN 17.893

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Hisamitsu SUZUKI
Serial No.: 09/691,456
Filed: October 18, 2000
Title: **SHALLOW TRENCH ISOLATION STRUCTURE
FOR A BIPOLAR TRANSISTOR**
Examiner: Samuel A. Gebremariam
Group Art Unit: 2811

Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT UNDER 37 C.F.R. 1.116

S I R :

In response to the Office Action mailed on July 1, 2002, the period for responding thereto having been set to expire after October 1, 2002, please amend the subject application as follows: